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PATENT NUMBER and
ISSUE DATE

U.S. UTILITY Patent Application

APPL NUM 10044365	FILING DATE 01/11/2002	CLASS 257	SUBCLASS 30	GAU 2811	EXAMINER C. Wang
**APPLICANTS: Wang Xiaobao; Sung Chiakang; Wang Bonnie; Nguyen Khai;					
**CONTINUING DATA VERIFIED: THIS APPLN CLAIMS BENEFIT OF 60/315,965 08/29/2001 OC					
** FOREIGN APPLICATIONS VERIFIED: OC					
PG-PUB <input checked="" type="checkbox"/> DO NOT PUBLISH		RESCIND <input type="checkbox"/>			
Foreign priority claimed <input type="checkbox"/> yes <input checked="" type="checkbox"/> no		35 USC 119 conditions met <input type="checkbox"/> yes <input checked="" type="checkbox"/> no		ATTORNEY DOCKET NO	
Verified and Acknowledged Examiners's Initials <i>OC</i>				015114-054910US	
TITLE : On-chip impedance matching circuit					

U.S. DEPT. OF COMM./PAT. & TM.-PTO-436L (Rev. 12-94)

NOTICE OF ALLOWANCE MAILED		CLAIMS ALLOWED	
		Total Claims	Print Claim for O.G.
ISSUE FEE		DRAWING	
Amount Due	Date Paid	Sheets Drwg.	Figs. Drwg.
		Print Fig.	
		Application Examiner	
<input type="checkbox"/> TERMINAL DISCLAIMER		PREPARED FOR ISSUE	
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